



## Eaton 106396

Eaton EC4P Compact PLC, 24 V DC, 12DI(of 4AI), 8DO(T), 1AO, CAN

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<b>PRODUCT NAME</b>	Eaton EC4P Compact PLC
<b>CATALOG NUMBER</b>	106396
<b>PRODUCT LENGTH/DEPTH</b>	72 mm
<b>PRODUCT HEIGHT</b>	90 mm
<b>PRODUCT WIDTH</b>	107.5 mm
<b>PRODUCT WEIGHT</b>	0.29 kg
<b>CERTIFICATIONS</b>	UL Category Control No.: NRAQ UL508 CSA Class No.: 2252-01 CSA File No.: 012528 UL File No.: E135462 CSA-C22.2 No. 142-M UL CSA-C22.2 No. 0-M CSA CE IEC/EN 61000-4-2, Level 3
<b>CATALOG NOTES</b>	Expandable: Inputs/outputs and bus systems

**FEATURES**

Parallel connection of transistor outputs with resistive load, inductive load with external suppressor circuit, combination within a group - Group 2: Q5 - Q8 Asynchronous, cyclic, acyclic PDO types (operating modes of the slave)

Parallel connection of transistor outputs with resistive load, inductive load with external suppressor circuit, combination within a group - Group 1: Q1 - Q4 Overload and short-circuit protection

190 received bytes in a block (PRG interface RS232, Master mode)

**AIR DISCHARGE**

8 kV

**10.10 TEMPERATURE RISE**

The panel builder is responsible for the temperature rise calculation. Eaton will provide heat dissipation data for the devices.

**10.11 SHORT-CIRCUIT RATING**

Is the panel builder's responsibility.

**10.12 ELECTROMAGNETIC COMPATIBILITY**

Is the panel builder's responsibility.

**10.13 MECHANICAL FUNCTION**

The device meets the requirements, provided the information in the instruction leaflet (IL) is observed.

**10.2.2 CORROSION RESISTANCE**

Meets the product standard's requirements.

**10.2.3.1 VERIFICATION OF THERMAL STABILITY OF ENCLOSURES**

Meets the product standard's requirements.

**10.2.3.2 VERIFICATION OF RESISTANCE OF INSULATING MATERIALS TO NORMAL HEAT**

Meets the product standard's requirements.

**10.2.3.3 RESIST. OF INSUL. MAT. TO ABNORMAL HEAT/FIRE BY INTERNAL ELECT. EFFECTS**

Meets the product standard's requirements.

**10.2.4 RESISTANCE TO**

Meets the product

**CHARACTERISTIC CURVE**

[eaton-electrical-timers-easy-control-relays-characteristic-curve.eps](#)

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[IL05003003Z](#)

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[eaton-modular-plc-easy-module-ec4p-compact-plc-dimensions.eps](#)

[eaton-general-approval-easy-control-relays-standards.jpg](#)

<b>ULTRA-VIOLET (UV) RADIATION</b>	standard's requirements.
<b>10.2.5 LIFTING</b>	Does not apply, since the entire switchgear needs to be evaluated.
<b>10.2.6 MECHANICAL IMPACT</b>	Does not apply, since the entire switchgear needs to be evaluated.
<b>10.2.7 INSCRIPTIONS</b>	Meets the product standard's requirements.
<b>10.3 DEGREE OF PROTECTION OF ASSEMBLIES</b>	Meets the product standard's requirements.
<b>10.4 CLEARANCES AND CREEPAGE DISTANCES</b>	Meets the product standard's requirements.
<b>10.5 PROTECTION AGAINST ELECTRIC SHOCK</b>	Does not apply, since the entire switchgear needs to be evaluated.
<b>10.6 INCORPORATION OF SWITCHING DEVICES AND COMPONENTS</b>	Does not apply, since the entire switchgear needs to be evaluated.
<b>10.7 INTERNAL ELECTRICAL CIRCUITS AND CONNECTIONS</b>	Is the panel builder's responsibility.
<b>10.8 CONNECTIONS FOR EXTERNAL CONDUCTORS</b>	Is the panel builder's responsibility.
<b>10.9.2 POWER-FREQUENCY ELECTRIC STRENGTH</b>	Is the panel builder's responsibility.
<b>10.9.3 IMPULSE WITHSTAND VOLTAGE</b>	Is the panel builder's responsibility.
<b>10.9.4 TESTING OF ENCLOSURES MADE OF INSULATING MATERIAL</b>	Is the panel builder's responsibility.
<b>FITTED WITH:</b>	Memory unit Documentation Engineering software Basic device Power supply Other components Analog input module Digital input module Function module Communication module easyNet/CANopen® on board Analog output module Digital output module Libraries
<b>OPERATING FREQUENCY</b>	40000 Operations/h at resistive load Depending on the suppressor circuit (Inductive load to EN

	60947-5-1, With external suppressor circuit, Max. switching frequency, max. duty factor)
<b>POLLUTION DEGREE</b>	2
<b>ACCURACY</b>	<p>1 %, Analog outputs at 25 °C</p> <p>± 3 %, of actual value, two devices (Analog Inputs)</p> <p>± 5 s/day (± 0.5 h/year), Real-time clock, normally</p> <p>± 2, (I7, I8, I11, I12) ± 0.12 V, of actual value, within a single device (Analog Inputs)</p> <p>2 %, Analog outputs at -25 °C - 55 °C</p>
<b>BURST IMPULSE</b>	<p>2 kV, Signal cable</p> <p>2 kV, Supply cable</p> <p>According to IEC/EN 61000-4-4, level 3</p>
<b>AIR PRESSURE</b>	1080 hPa (operation)
<b>BUS TERMINATION</b>	EASY-NT-R plug (incl. bus terminating resistor 120 Ω), first and last station, CANopen®
<b>ENVIRONMENTAL CONDITIONS</b>	<p>Condensation: prevent with appropriate measures</p> <p>Clearance in air and creepage distances according to EN 50178, UL 508, CSA C22.2, No. 142</p>
<b>INDICATION</b>	<p>LCD-display used as status indication of Digital inputs</p> <p>24 V DC</p> <p>LCD-display used as Output status indication of Transistor outputs</p>
<b>INPUT</b>	Voltage (DC)
<b>CABLE LENGTH</b>	<p>30 m, screened, Analog inputs</p> <p>100 m, unshielded, Digital inputs</p> <p>24 V DC</p>
<b>OUTPUT VOLTAGE</b>	<p><math>U = U_e - 1 \text{ V}</math> (signal 1 at <math>I_e = 0.5 \text{ A}</math>, transistor outputs)</p> <p>Max. 2.5 V (at signal 0 at external load &lt; 10 MΩ, transistor outputs)</p>
<b>MOUNTING METHOD</b>	<p>Top-hat rail fixing (according to IEC/EN 60715, 35 mm)</p> <p>Screw fixing using fixing brackets ZB4-101-GF1 (accessories)</p>

<b>NUMBER OF OUTPUTS</b>	Max. 4 (for parallel connection) 8 Transistor Outputs
<b>CHARACTER FORMATS</b>	8E1, 8O1, 8N1, 8N2, 7E2, 7O2, 7N2, 7E1, PRG interface RS232, Master mode
<b>SCREWDRIVER SIZE</b>	3.5 x 0.8 mm, Terminal screw
<b>MOUNTING POSITION</b>	Horizontal Vertical
<b>OUTPUT</b>	Voltage (DC)
<b>CONTACT DISCHARGE</b>	6 kV, Electrostatic discharge (ESD)
<b>OVERVOLTAGE CATEGORY</b>	II
<b>CONNECTION TYPE</b>	RJ45, Ethernet RJ45, PRG Interface RS232 2 x RJ45, 8 pole, CANopen®
<b>DUTY FACTOR</b>	100 % (Inductive load to EN 60947-5-1, Without external suppressor circuit, $T_{0.95} = 15 \text{ ms}$ , $R = 48 \Omega$ , $L = 0.24 \text{ H}$ ) 100 % (Inductive load to EN 60947-5-1, With external suppressor circuit) 100 % (Inductive load to EN 60947-5-1, Without external suppressor circuit, $T_{0.95} = 1 \text{ ms}$ , $R = 48 \Omega$ , $L = 16 \text{ mH}$ ) 100 % (Inductive load to EN 60947-5-1, Without external suppressor circuit, DC-13, $T_{0.95} = 72 \text{ ms}$ , $R = 48 \Omega$ , $L = 1.15 \text{ H}$ )
<b>PEAK SHORT-CIRCUIT CURRENT</b>	32 A
<b>CONSTANT ACCELERATION</b>	2 g, 57 - 150 Hz, according to IEC/EN 60068-2-6, Vibrations
<b>CONSTANT AMPLITUDE</b>	0,15 mm, 10 - 57 Hz, according to IEC/EN 60068-2-6, Vibrations
<b>SURGE RATING</b>	According to IEC/EN 61000-4-5, power pulses (Surge), EMC 2 kV, Supply cables, symmetrical, EASY...AC, power pulses (Surge), EMC  0.5 kV, Supply cables,

	symmetrical, EASY...DC, power pulses (Surge), EMC
<b>TERMINAL CAPACITY (FLEXIBLE WITH FERRULE AWG)</b>	22 - 12
<b>CONVERSIONS</b>	Each CPU cycle, Analog inputs Each CPU cycle, Analog outputs
<b>ELECTROMAGNETIC FIELDS</b>	10 V/m (according to IEC EN 61000-4-3)
<b>PROTECTION AGAINST POLARITY REVERSAL</b>	Yes (Caution: A short circuit will result if 0 V or earth is applied to the outputs in the event that the supply voltage is connected to the wrong poles.)
<b>TERMINAL CAPACITY (SOLID AWG)</b>	22 - 12
<b>CURRENT CONSUMPTION</b>	2 A, max. total current of Transistor outputs (Caution! Outputs must be actuated simultaneously and for the same length of time.)
<b>NUMBER OF INPUTS (ANALOG)</b>	4 (I7, I8, I11, I12)
<b>LOAD RESISTANCE</b>	1 kΩ
<b>CYCLE TIME</b>	< 0,3 ms, for 1 k of instructions (Bit, Byte), CPU
<b>NUMBER OF MODULES</b>	Max. 126 (slaves)
<b>DROP AND TOPPLE</b>	50 mm Drop height, Drop to IEC/EN 60068-2-31
<b>IMMUNITY TO LINE- CONDUCTED INTERFERENCE</b>	10 V (according to IEC/EN 61000-4-6)
<b>RADIO INTERFERENCE CLASS</b>	Class B (EN 55011) Class B (EN 55022)
<b>DATA TRANSFER RATE</b>	125 kBit/s at 125 m, CANopen® 57.6 kBit/s, PRG interface RS232, Master mode 500 kBit/s at 25 m, CANopen® 10 MBit/s, 100 m, Ethernet  0.3 kBit/s, PRG interface RS232, Master mode 1.2 kBit/s, PRG interface RS232, Master mode 20 kBit/s at 700 m, CANopen®

	0.6 kBit/s, PRG interface RS232, Master mode 2.4 kBit/s, PRG interface RS232, Master mode 10 kBit/s at 1000 m , CANopen® 250 kBit/s at 60 m, CANopen® 4.8 kBit/s, PRG interface RS232, Master mode 50 kBit/s at 300 m, CANopen® 9.6 kBit/s, PRG interface RS232, Master mode 38.4 kBit/s, PRG interface RS232, Master mode 19.2 kBit/s, PRG interface RS232, Master mode
<b>RELATIVE HUMIDITY</b>	5 - 95 % (non-condensing)
<b>DEGREE OF PROTECTION</b>	IP20
<b>DELAY TIME</b>	0.02 ms typ., Digital inputs 24 DC (I1 - I4), Delay time from 0 to 1 0.25 ms typ., Digital inputs 24 DC (I5 - I12), Delay time from 0 to 1
<b>RESIDUAL CURRENT</b>	0.1 mA (on signal "1" per channel)
<b>RESIDUAL RIPPLE</b>	5 % (transistor outputs) ≤ 5 %
	50 kHz, Counter frequency
<b>RAPID COUNTER INPUTS</b>	16/32 Bit (value range) 2 (I1, I2) at 16 Bit or 1 (I1) at 32 Bit ≤ 20 m (cable length, screened) Square (pulse shape)
<b>RECOVERY TIME</b>	100 μ s
<b>RATED OPERATIONAL CURRENT (IE)</b>	Max. 0.5 A at signal „1“ DC per channel
<b>INSULATION RESISTANCE</b>	According to EN 50178
<b>FUNCTIONS</b>	Thermal cutout Building blocks
<b>HEAT DISSIPATION</b>	3.4 W
<b>SUPPLY VOLTAGE AT DC - MAX</b>	24 VDC
<b>RESOLUTION</b>	<ul style="list-style-type: none"> <li>• 0.01 V analog (Analog inputs)</li> <li>• 0.01 V DC analog (Analog outputs)</li> <li>• 0.01 V digital (Analog inputs)</li> </ul>

	<ul style="list-style-type: none"> <li>• 10 Bit (value 0 - 1023, Analog inputs)</li> <li>• 10 Bit (value 0 - 1023, digital, Analog outputs)</li> </ul>
<b>INCREMENTAL COUNTER</b>	Value range: 32 Bit Reference input: I3 Signal offset: 90° Counter frequency: ≤ 40 kHz Pulse shape: Square Number of counter inputs: 1 (I1, I2, I3, I4) Counter inputs: I1, I2 Input for reference switch: I4
<b>SHORT-CIRCUIT CURRENT</b>	16 A, Transistor outputs
<b>STATION</b>	To DS 301 V4, Control contact rated current, Mode slave, Interfaces
<b>INPUT CURRENT</b>	3.3 mA (I1 - I6, at 24 V DC, at signal 1) 1 mA (Analog inputs) 2.2 mA (I7 - I8, at 24 V DC, at signal 1) 3.3 mA (I9 - I10, at 24 V DC, at signal 1) 2.2 mA (I11 - I12, at 24 V DC, at signal 1) 140 mA
<b>INPUT IMPEDANCE</b>	11.2 kΩ
<b>INPUT VOLTAGE</b>	Signal 1: > 15 V DC (I1 - I6, I9 - I10, Digital inputs, 24 V DC) Signal 0: < 8 V DC (I7 - I8, I11 - I12, Digital inputs, 24 V DC) Signal 1: > 8 V DC (I7 - I8, I11 - I12, Digital inputs, 24 V DC) Signal 0: < 5 V DC (I1 - I6, I9 - I10, Digital inputs, 24 V DC)
<b>PROCESSOR</b>	Infineon XC161
<b>SHOCK RESISTANCE</b>	15 g, Mechanical, according to IEC/EN 60068-2-27, Half-sinusoidal shock 11 ms, 18 Impacts
<b>INSCRIPTION</b>	Individual inscription possible with EC4-COMBINATION-*
<b>SHORT-CIRCUIT</b>	$0.7 \leq I_e \leq 2$ per output, For



<b>TRIPPING CURRENT</b>	Ra ≤ 10 mΩ, Transistor outputs
<b>LAMP LOAD</b>	5 W (without Rv per channel)
<b>SIGNAL RANGE</b>	0 - 10 V DC, Analog inputs
<b>SUPPLY CURRENT</b>	18/32 mA, Normally/max., On 0 signal, Transistor outputs 24/44 mA, Normally/max., On 1 signal, Transistor outputs
<b>MEMORY</b>	4 kByte Output Memory 16 kByte Marker Memory 4 kByte Input Memory 256 kByte Program memory code 8 kByte Retain Memory 14 segments of 16 kByte Program memory data
<b>NUMBER OF BYTES</b>	190 transmission bytes (in a block)
<b>NUMBER OF INPUTS (DIGITAL)</b>	12 4 (can also be used as analog inputs) 12 (24 V DC) 4 (I7, I8, I11, I12, can also be used as analog inputs)
<b>UTILIZATION FACTOR</b>	0.25 (Inductive load to EN 60947-5-1, Without external suppressor circuit, DC-13, T0.95 = 72 ms, R = 48 Ω, L = 1.15 H) 0.25 (Inductive load to EN 60947-5-1, Without external suppressor circuit, T0.95 = 1 ms, R = 48 Ω, L = 16 mH) 0.25 (Inductive load to EN 60947-5-1, Without external suppressor circuit, T0.95 = 15 ms, R = 48 Ω, L = 0.24 H) 1 (Inductive load to EN 60947-5-1, With external suppressor circuit)
<b>VOLTAGE DIPS</b>	≤ 10 ms According to EN 61131-2
<b>POTENTIAL ISOLATION</b>	Supply voltage UAUX: yes Between Digital inputs 24 V DC and Outputs: yes Between Analog inputs and Outputs: yes Between Transistor outputs and Inputs: yes Between Digital inputs 24 V DC and network

	<p>easyNet, easyLink</p> <p>Between Transistor outputs and Memory card: yes</p> <p>Between Transistor outputs and Power supply: yes</p> <p>Between Analog inputs and Interface/memory card: no</p>
<b>RATED OPERATIONAL VOLTAGE</b>	<p>24 V DC (-15 %/+ 20 % - power supply)</p> <p>20.4 - 28.8 V DC (Transistor outputs)</p> <p>20.4 - 28.8 V DC</p>
<b>SHORT-CIRCUIT PROTECTION</b>	<p>Yes, electronic (Q1 - Q4), thermal (Q5 - Q8), (analysis via diagnostics input I16, I15), Transistor outputs</p>
<b>TERMINAL CAPACITY (FLEXIBLE WITH FERRULE)</b>	0.2/2.5 mm <sup>2</sup>
<b>SWITCHING FREQUENCY</b>	<p>Max. 1500 Operations (Inductive load to EN 60947-5-1, without external suppressor circuit, DC-13, T0.95 = 72 ms, R = 48 Ω, L = 1.15 H, f = 0.5 Hz (max. DF = 50 %))</p> <p>Max. 1500 Operations (Inductive load to EN 60947-5-1, without external suppressor circuit, T0.95 = 15 ms, R = 48 Ω, L = 0.24 H, f = 0.5 Hz (max. DF = 50 %))</p> <p>Max. 1500 Operations (Inductive load to EN 60947-5-1, without external suppressor circuit, T0.95 = 1 ms, R = 48 Ω, L = 16 mH, f = 0.5 Hz (max. DF = 50 %))</p>
<b>TERMINAL CAPACITY (SOLID)</b>	0.2/4 mm <sup>2</sup>
<b>TIGHTENING TORQUE</b>	0.6 Nm
<b>WRITE CYCLES OF THE RETENTIVE MEMORY</b>	10,000,000,000 read-write cycles
<b>AMBIENT OPERATING TEMPERATURE - MAX</b>	55 °C
<b>AMBIENT OPERATING TEMPERATURE - MIN</b>	-25 °C
<b>AMBIENT STORAGE TEMPERATURE - MAX</b>	70 °C

AMBIENT STORAGE TEMPERATURE - MIN	-40 °C
DISPLAY TEMPERATURE - MAX	55 °C
DISPLAY TEMPERATURE - MIN	0 °C
EQUIPMENT HEAT DISSIPATION, CURRENT-DEPENDENT PVID	0 W
HEAT DISSIPATION CAPACITY PDISS	0 W
HEAT DISSIPATION PER POLE, CURRENT-DEPENDENT PVID	0 W
HEIGHT OF FALL (IEC/EN 60068-2-32) - MAX	1 m
NUMBER OF OUTPUTS (ANALOG)	1
OUTPUT CURRENT (MA) - MAX	100 mA
RATED OPERATIONAL CURRENT FOR SPECIFIED HEAT DISSIPATION (IN)	0 A
RATED OPERATIONAL VOLTAGE (UE) AT DC - MAX	24 V
STATIC HEAT DISSIPATION, NON-CURRENT-DEPENDENT PVS	3.4 W

PROJECT NAME:
PROJECT NUMBER:
PREPARED BY:



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